

IN THE SPECIFICATION

Please replace the Title paragraph at page 1, line 1, with the following rewritten Title:

AUTO-FOCUS APPARATUS BASED ON PIXEL RESOLUTION,
AND CONTROL METHOD FOR THE SAME

Please replace the paragraph at page 6, lines 8-25, with the following rewritten paragraph:

Fig. 1 is a block diagram showing a digital camera with an auto-focus apparatus according to an embodiment of the present invention applied therein. In this figure, the reference numeral 100 ~~indicated~~ indicates the digital camera. This digital camera 100 comprises a lens system 101, a mechanical system 102 including a lens aperture and a filter or the like, a CCD (Charge-Coupled Device) 103, a CDS (Correlation Double Sampling) circuit 104, an automatic gain control amplifier (AGC amplifier) 105, an A/D converter 106, an IPP (Image Pre-Processor) 107, a DCT (Discrete Cosine Transform) 108, a coder 109, ~~an a~~ an MCC (Memory Card Controller) 110, a DRAM 111, a PC (personal Computer) card interface 112, a CPU (Central Processing Unit) 121, a display section 122, an operating section 123, a SG (control signal generating) section 126, ~~an a~~ a strobe 127, a ~~battery~~ battery 128, a DC-DC converter 129, an EEPROM 130, a focus driver 131, a pulse motor 132, a zoom driver 133, a pulse motor 134, and a motor driver 135. A detachable PC card 150 can be connected via the PC card interface 112 to this digital camera 100.

Please replace the paragraph at page 11, lines 12-20, with the following rewritten paragraph:

Next description is made for auto-focus control. For the auto-focus control, after a shutter speed and a gain are set, the pulse motor 132 is driven according to specified pulses

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for a 1Vd period. When ~~this~~ the specified pulses ~~is~~ are being ~~driven~~ provided, a digital video signal obtained in the IPP 107 is processed to obtain a brightness signal. High-frequency components of this brightness signal are integrated to obtain an AF evaluated value, and the peak of this AF evaluated value is considered as a focus position.

Please replace the paragraph at page 24, line 17, to page 25, line 7, with the following rewritten paragraph:

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When the release key RL is ON, the CPU 121 executes the above mentioned auto-focusing (step S31). Then, the CPU 121 locks the auto-focus (fix the focus position), and ~~disable change of~~ disables changing the number of recording pixels in the CCD 103 on recording (step S32). Then, the CPU 121 determines whether the RL2 has been turned ON or not (step S33). When it is determined that the RL2 has been turned ON, the CPU 121 starts recording. On the other hand, when it is determined that the RL2 is not turned ON, the processing is shifted to step S36. In step S36, the CPU 121 determines whether the RL1 has been turned ON or not. When it is determined that the RL1 has been turned ON, the processing is returned to step S33. When it is determined that the RL1 is not turned ON, the CPU 121 releases the auto-focus lock, ~~permit~~ and permits the change of the number of recording pixels in the CCD 103 on recording (step S35). Then the processing is returned to step S30.
